

CALCE Web Seminar:

Uprating of Capacitors

Date and Time:

Dec. 19th start at 11:00am U.S. Eastern (8:00am U. S. Pacific)

Free for CALCE EPSC members

Register on-line at

<http://www.calce.umd.edu/seminars/present/UpratingofCapacitors.htm>

An analysis and electrical parameter characterization plan for passive electronic components over temperature, voltage and frequency has been developed and demonstrated. The methods and results can be used to determine if a part can be used beyond the manufacturer specified rating conditions.

In this presentation this methodology will be described with examples of implementation on multilayered ceramic capacitors; and tantalum and niobium capacitors. Examples of results from other capacitors will also be presented.

About the presenter: Dr. Diganta Das (Ph.D., Mechanical Engineering, University of Maryland, College Park, B.Tech, Manufacturing Science and Engineering, Indian Institute of Technology) is a member of the research staff at the Center for Advanced Life Cycle Engineering. His expertise is in reliability, environmental and operational ratings of electronic parts, uprating, electronic part reprocessing, technology trends in the electronic parts and parts selection and management methodologies. He performs benchmarking processes and organizations of electronics companies for parts selection and management and reliability practices. He also assists organizations in design improvements.



Dr. Das has published more than 50 articles on these subjects, and presented his research at international conferences and workshops. He had been the technical editor for two IEEE standards and is currently coordinator for two additional IEEE standards.

He is an editorial board member for the journal Microelectronics Reliability and International Journal for Performability Engineering. He is a Six Sigma Black Belt and a member of IEEE and IMAPS.

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