

# Symposium on Avoiding, Detecting and Preventing Counterfeit Electronics Parts

**December 2, 2009**

## Session I: Introduction and Scope

Time	Topic	Presenter
8:00am – 8:10am	Welcome and Introduction	Dr. Diganta Das, <i>CALCE/ University of Maryland</i>
8:10am – 8:35am	The Scope and Magnitude of the Problem	Mark Crawford, <i>US Department of Commerce</i>
8:35am – 9:05am	Counterfeiters Techniques are Constantly Improving to Avoid Detection – Our National Security is Dependent on Us to Keep Up	Tom Sharpe, <i>SMT Corporation</i>
9:05am – 9:35am	AS5553 – A New Standard in the Fight Against Counterfeit Parts	Dan DiMase, <i>Honeywell</i>
9:35am – 10:00am	International Property Rights Enforcement in the Electronics Industry	Craig Thurber, <i>U.S. Immigration &amp; Customs Enforcement</i>
10:00am – 10:30am	Break	

## Session II: Legal and Law Enforcement Issues

Time	Topic	Presenter
10:30am – 10:50am	Intellectual Property – The Primary Legal Principle Behind Counterfeit Definitions	Prof. Patricia Campbell, <i>University of Maryland</i>
10:50am – 11:10am	Counterfeit Components and Related Legal Issues	Larry Pappas, <i>Channel One International</i>
11:10am – 11:30am	Setting up Anti-Counterfeiting Technologies that will Stand Up in Court and are Cost Effective	David Kalow, <i>Kalow &amp; Springut LLP</i>
11:30am – 12:00pm	Global Perspective on Intellectual Property Issues	Tim Trainer, <i>Global Intellectual Property Strategy Center, P.C.</i>
12:00pm – 12:30pm	Intellectual Property Rights Enforcement	Therese Randazzo, <i>U.S. Customs and Border Protection</i>
12:30pm - 1:30pm	Lunch	

1:30pm – 5:00pm	<p style="color: #A52A2A;"><b>Choice of three workshops</b></p> <p>1.) <i>Counterfeit Parts Avoidance Training for EEE Parts</i></p> <p>2.) <i>Anti-Counterfeiting: The Powers That Be – An IP and Legal Perspective</i></p> <p>3.) <i>Physical, Material, and Electrical Testing Techniques in the Laboratory to Identify and Mitigate EEE Counterfeit Risk</i></p>
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## Symposium on Avoiding, Detecting and Preventing Counterfeit Electronics Parts

**December 3, 2009**

### Session III: Supply Chain Perspective

Time	Topic	Presenter
8:00am – 8:30am	Counterfeit Electronics – It is Beyond Components	Dr. Diganta Das, <i>CALCE / University of Maryland</i>
8:30am – 9:00am	Quality & Reliability Issues with Counterfeit ICs	Andrew Olney, <i>Analog Devices</i>
9:00am – 9:20am	Franchised Distribution Role to Mitigate Risk Associated with Counterfeit Parts	Bill Palladino, <i>Arrow Electronics</i>
9:20am – 9:40am	Independent Distributor Perspective: Challenges & Solutions to Mitigate Counterfeit Parts	Steve Calabria, <i>Independent Distributors of Electronics Association (IDEA)</i>
9:40am – 10:00am	Risk Mitigation Tools to Address Today's Complex Global Marketplace	Mark Snider, <i>ERAI, Inc.</i>
10:00am – 10:30am	Break	

### Session IV: Technical Tools and Methods

Time	Topic	Presenter
10:30am – 11:00am	Laser Surface Authentication: Natural Randomness as a Fingerprint for Product Authentication	Mark McGlade, <i>Ingenia Technology Limited</i>
11:00am – 11:30am	Electrical Testing for Counterfeit Detection	Mark Marshall, <i>Integra Technologies</i>
11:30am – 12:00pm	Understanding Industry Perspective in the FAR Council Effort to Create Assurance in the Supply Chain	Trey Hodgkins, <i>TechAmerica</i>
12:00pm – 12:30pm	First Line of Defense Against Counterfeits	Robb Hammond, <i>American Electronic Resource, Inc.</i>
12:30pm – 1:15pm	Lunch	

1:15pm – 2:45pm	<b>Supply Chain Panel Discussion –</b> <b>Moderator:</b> Lisa Gardner, <i>Government Accountability Office</i> <b>Panelists:</b> Lonnie Hurst, <i>Intel Corporation</i> ; Art Mester, <i>Boeing Company</i> ; Bill Palladino, <i>Arrow Electronics</i> ; Tom Sharpe, <i>SMT Corporation</i> ; Mark Snider, <i>ERAI</i>	
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### Session V: Government and Defense Perspective

Time	Topic	Presenter
3:00pm – 3:20pm	The Black Swan Effect	Jack Stradley, <i>Rochester Electronics</i>

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3:20pm – 3:40pm	Counterfeit Parts Risk Mitigation	Charlie Whitmeyer, <i>Orbital Sciences Corporation</i>
3:40pm – 4:00pm	Mitigating Counterfeit Issues through GIDEP	Robert Karpen, <i>GIDEP</i>
4:00pm – 4:20pm	Microelectronic and Semiconductor Vulnerabilities, a Defense Supply Center Columbus (DSCC) Approach	Ernest Reid and Michael Adams, <i>Defense Logistics Agency</i>
4:20pm – 4:40pm	Industry Recommendations on Mitigating the Risk Posed by Counterfeit Parts	Art Mester, <i>Boeing Company</i>
4:40pm – 5:00pm	The Way Forward	Brian Hughitt, <i>NASA</i>